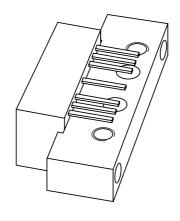
DISCRETE SEMICONDUCTORS

DATA SHEET



CGY887A 860 MHz, 25.5 dB gain push-pull amplifier

Product specification Supersedes data of 2001 Oct 25 2002 Apr 18





860 MHz, 25.5 dB gain push-pull amplifier

CGY887A

FEATURES

- · High gain
- Superior linearity
- · Extremely low noise
- · Rugged construction
- Gold metallization ensures excellent reliability.

APPLICATIONS

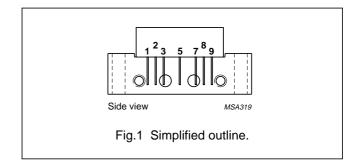
 CATV systems operating in the 40 to 870 MHz frequency range.

DESCRIPTION

Hybrid dynamic range amplifier module in a SOT115J package operating with a voltage supply of 24 V (DC), employing both GaAs and Si dies.

PINNING - SOT115J

PIN	DESCRIPTION	
1	input	
2	common	
3	common	
5	+V _B	
7	common	
8	common	
9	output	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	25.2	25.8	dB
		f = 870 MHz	25.7	27	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	_	240	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
Vi	RF input voltage	_	75	dBmV
T _{stg}	storage temperature	-40	+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C

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CHARACTERISTICS

Bandwidth 40 to 870 MHz; V_B = 24 V; T_{case} = 30 °C; Z_S = Z_L = 75 Ω .

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	25.2	25.8	dB
		f = 870 MHz	25.7	27	dB
SL	straight line	f = 40 to 870 MHz	0.5	1.4	dB
FL	flatness of frequency response	f = 40 to 870 MHz	_	±0.5	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	_	dB
		f = 80 to 160 MHz	20	_	dB
		f = 160 to 320 MHz	20	_	dB
		f = 320 to 550 MHz	20	_	dB
		f = 550 to 640 MHz	19	_	dB
		f = 640 to 750 MHz	17	_	dB
		f = 750 to 870 MHz	17	_	dB
\$ ₂₂	output return losses	f = 40 to 80 MHz	21	_	dB
		f = 80 to 160 MHz	19	_	dB
		f = 160 to 320 MHz	17	_	dB
		f = 320 to 550 MHz	16	_	dB
		f = 550 to 640 MHz	16	_	dB
		f = 640 to 750 MHz	16	_	dB
		f = 750 to 870 MHz	16	_	dB
S ₂₁	phase response	f = 50 MHz	-45	+45	deg
СТВ	composite triple beat	129 channels flat; V _o = 40 dBmV; measured at 745.25 MHz	_	-62	dB
X _{mod}	cross modulation	129 channels flat; V _o = 40 dBmV; measured at 55.25 MHz	_	-56	dB
CSO	composite second order distortion	129 channels flat; V _o = 40 dBmV; measured at 860.5 MHz	-	-59	dB
		129 channels flat; V _o = 40 dBmV; measured at 150 MHz	-	-69	dB
d ₂	second order distortion	note 1	_	-67	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$; note 2	62	_	dBmV
NF	noise figure	f = 50 MHz	_	5.5	dB
		f = 100 to 870MHz	_	5	dB
I _{tot}	total current consumption (DC)	note 3	_	240	mA

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Notes

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1. f_p = 55.25 \text{ MHz}; V_p = 50 \text{ dBmV}; f_q = 805.25 \text{ MHz}; V_q = 50 \text{ dBmV}; measured at f_p + f_q = 860.5 \text{ MHz}.
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2. Measured according DIN45004B:

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\begin{split} f_p &= 851.25 \text{ MHz; } V_p = V_o; \\ f_q &= 858.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 860.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 849.25 \text{ MHz.} \end{split}
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3. The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

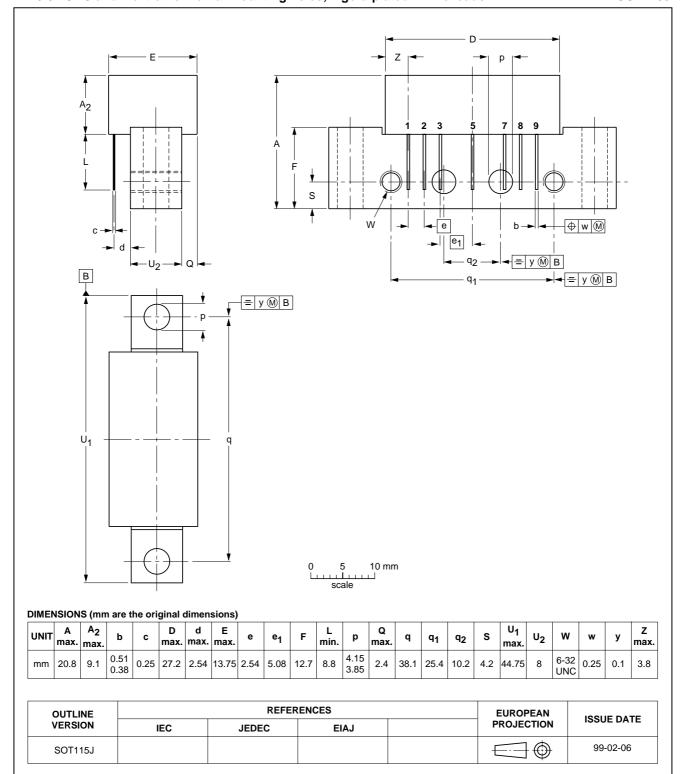
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PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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NOTES

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